

Atty. Dkt. No. 039153-0256 (F0113)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Yu

Title:

A PROCESS FOR

MANUFACTURING

TRANSISTORS HAVING SILICON/GERMANIUM CHANNEL

REGIONS

Appl. No.:

09/599,141

Filing

06/22/2000

Date:

Examiner:

Roman, A.

Art Unit:

2812

AMENDMENT TRANSMITTAL

Commissioner for Patents Box AF Washington, D.C. 20231

Sir:

Transmitted herewith is an amendment in the above-identified application.

- [] Small Entity status under 37 C.F.R. § 1.9 and § 1.27 has been established by a Small Entity statement previously submitted.
- [] Small Entity statement is enclosed.
- [X] The fee required for additional claims is calculated below:

	Claims as Amended		Previously Paid For		Extra Claims Presen	3	Rate		Additional Claims Fee
Total Claims:	26	_	26	=	0	×	\$18.00	=	\$0.00
Independents:	4	_	4	=	0	— ×	\$84.00	=	\$0.00
First presentation of any Multiple Dependent Claims: + \$280.00						=	\$0.00		
						CLAIMS	FEE TOTAL:	=	\$0.00

[] Applicant hereby petitions for an extension of time under 37 C.F.R. §1.136(a) for the total number of months checked below:

CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as First Class Mail in an envelope addressed to the Commissioner for Patents, Washington, D.C. 20231.

Span (Printed Name)

(Signature)

(Date of Deposit)

Atty. Dkt. No. 039153-0256 (F0113)

Extension for response filed within the first month:	\$110.00	\$0.00
Extension for response filed within the second month:	\$400.00	\$0.00
Extension for response filed within the third month:	\$920.00	\$0.00
Extension for response filed within the fourth month:	\$1,440.00	\$0.00
Extension for response filed within the fifth month:	\$1,960.00	\$0.00
EXTENSĮON	FEE TOTAL:	\$0.00
CLAIMS AND EXTENSION	FEE TOTAL:	\$0.00
Small Entity Fees Apply (subtract	½ of above):	\$0.00
	TOTAL FEE:	\$0.00
	Extension for response filed within the second month: Extension for response filed within the third month: Extension for response filed within the fourth month: Extension for response filed within the fifth month: EXTENSION CLAIMS AND EXTENSION	Extension for response filed within the second month: \$400.00 Extension for response filed within the third month: \$920.00 Extension for response filed within the fourth month: \$1,440.00 Extension for response filed within the fifth month: \$1,960.00 EXTENSION FEE TOTAL: CLAIMS AND EXTENSION FEE TOTAL: Small Entity Fees Apply (subtract ½ of above):

- [] Please charge Deposit Account No. 06-1447 in the amount of \$0.00. A duplicate copy of this transmittal is enclosed.
- [] A check in the amount of \$0.00 is enclosed.
- [X] The Commissioner is hereby authorized to charge any additional fees which may be required regarding this application under 37 C.F.R. §§ 1.16-1.17, or credit any overpayment, to Deposit Account No. 06-1447. Should no proper payment be enclosed herewith, as by a check being in the wrong amount, unsigned, post-dated, otherwise improper or informal or even entirely missing, the Commissioner is authorized to charge the unpaid amount to Deposit Account No. 06-1447.

Please direct all correspondence to the undersigned attorney or agent at the address indicated below.

Respectfully submitted,

FOLEY & LARDNER

Suite 3800

777 East Wisconsin Avenue

Milwaukee, Wisconsin 53202-5306

Telephone: (414) 297-5768

Facsimile:

(414) 297-4900

Joseph N. Ziebert

Attorney for Applicant

Registration No. 35,421



• #10/C

Atty. Dkt. No. 039153-0256 (F01/14)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Yu

Title:

A PROCESS FOR MANUFACTURING

TRANSISTORS HAVING

SILICON/GERMANIUM CHANNEL

REGIONS

Appl. No.:

09/599,141

Filing

06/22/2000

Date:

Examiner:

Roman, A.

Art Unit:

2812

AMENDMENT

Commissioner for Patents Box AF Washington, D.C. 20231

Sir:

This communication is responsive to the Office Action dated October 24, 2002, concerning the above-referenced patent application.

Please amend the application as follows:

IN THE CLAIMS:

In accordance with 37 C.F.R. § 1.121, please substitute for original claim 11, the following rewritten versions of the same claims, as amended. The changes are shown explicitly in the attached "Version with Markings to Show Changes Made".

11. (Twice Amended) The method of claim 1, further comprising:

providing a second amorphous semiconductor material above the amorphous semiconductor material including germanium after the laser annealing step;

performing another laser annealing step to form a second single crystalline semiconductor layer from the second amorphous semiconductor material; and

1